

i.MX 6Quad/6Dual Personality Fuses

1 Introduction

This document describes on-chip fuses (called personality fuses) that are used to define the characteristics of the i.MX 6Quad and 6Dual SoCs.

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2 i.MX 6Quad/6Dual Personality Fuses

Table 1 shows the fuses related to the definition of the SoC.

Table 1. Personality Fuses

Fuse Function	Fuse Address	OCOTP Address	Fuse Name	Settings			
Quad/ Dual	0x430[21:20]	0x021B_C430[21:20]	NUM_CORES[1:0]	00 – 4x Cores	01 – Reserved	10 – 2x Cores	11 – Reserved
				MCIMX6Qxxxxxxx		MCIMX6Dxxxxxxx	
MLB	0x430[26]	0x021B_C430[26]	MLB_DISABLE	0 – Enabled		1 – Disabled	
				MCIMX6x4xxxxxxx MCIMX6x5xxxxxxx MCIMX6x6xxxxxxx		MCIMX6x7xxxxxxx	
VPU	0x440[15]	0x021B_C440[15]	VPU_DISABLE	0 – Enabled		1 – Disabled	
				MCIMX6x5xxxxxxx MCIMX6x6xxxxxxx MCIMX6x7xxxxxxx		MCIMX6x4xxxxxxx	
Market Segment	0x480[7:6]	0x021B_C480[7:6]	Market Segment[1:0]	00 – Reserved	01 – Extended commercial -20C to 105C	10 – Industrial -40C to 105C	11 – Automotive -40C to 125C
					MCIMX6xxExxxxxx	MCIMX6xxCxxxxx	MCIMX6xxAxxxxx
Speed	0x440[17:16]	0x021B_C440[17:16]	SPEED GRADING[5:4]	00 – 800 MHz (Industrial grade)	01 – 852 MHz (Automotive grade)	10 – 1000 MHz	11 – 1200 MHz
				MCIMX6xxxxx00xx	MCIMX6xxxxx01xx	MCIMX6xxxxx10xx	MCIMX6xxxxx11xx
HDCP	0x460[16]	0x021B_C460[16]	HDCP ENCRYPTION DISABLE	0 – Encryption enabled		1 – Encryption disabled	
				MCIMX6xxxxxxCx		MCIMX6xxxxxxAx	

3 Revision History

Revision 0 is the initial release of this document.

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